1	Date	Semester	Subject Name	Time	data.	Marks	
06/04	2018	III (Repeater)	File structure and Database Management	02.30 PM	ince	75	
Instr	uction.		lo. 1 is compulsory	- Aq.	nicki i Arteri i		20
		II)Attempt a	ny faur from Q.2 to Q.8				
0.1	a) Def	the following			E.	(10)	1
		2) Sparse Indi		erial Libitty	F 1	日際	
	to Mich	4) Hashing	5) B - Tree		244	(5)	13
	o, ven	it is the ? Expi	ain types of file		113.0	THE	111
2.7	a) Expl	ain ACIO prop	erties of Transaction		Benjania	(8)	14
101150			locking protocol		197世紀	(7)	
		(1)		- 1	4.1.80	18 4	
2.3	-) Expl	alr steps of Q	uery Processing	ei Poit	3,000	(8)	
			Explain Collision disclusion techn	laties .	The live	17	
					. All .	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	1
1.4	a) Expli	in Secondary	Index with the mail the same	<b>3</b> 11 (6) 4		19	100
	b) Expk	ein Materialia	HICH S.M. C. HAVE			THE STATE OF	
				E mer till like	A SHIP		
Q.5		- APP 12/2 11/2	e und 1916 (au a nilatripinti	WHIT TEST	E STA		
	b) Expli	ain Thornas vi	tree tripe		能 對門		體情
	1.01		The state of the s		Wil		1
Q.5			and translave tunto a query tre	e and space of	州 是		13
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	et a constant		ent (deptno, det thame)				33
	(20ler		of those student farnes whose	Interes and the	nine Siuc	40.7	130
		in IT depar			LARL.		4
	D) WEA	I IS IOCK I END	ain types of lock:?		W W W	124	6
				Mall In	1	1 , 2	1
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Day & Date	Semester	Subject Name	Time	Code	Marks
Tuesday 02/04/2019	III (Repeater)	Computer Organization and Architecture	11.00 AM To 01.30 PM	3103	75

- I. Q.1 is compulsory.
- II. Solve any four questions from Q.2 to Q.8.
- III. All questions carry equal marks.
- IV. Draw neat and proper diagrams if necessary.

Q.1.	a) Describe characteristics of Disk System.	[8]
	b) State different types of Semi-Conductors Memories.	[7]
	the transfer of the second sec	
Q.2.	a) Draw and explain the basic components of a computer	
	System.	[7]
	b) Explain Programmed I/O with the help of a Flow chart?	[8]
	***	-
Q.3.	a) What is RISC? Describe RISC pipeline with a suitable	
	Example	[8]
	b) Explain elements of Bus Design?	[7]
Q.4.	a) What is Mapping Function? Explain direct mapping Technique of cache m	emory
	design?	[8]
	b) Describe the function of I/O Module?	[7]
	P	
Q.5.	a) Explain the cache read operation using flow chart?	[7]
	b) Discuss DMA Function	[8]
	*	
Q.6.	a) Describe the memory hierarchy with diagram?	[8]
	b) Differentiate between Magnetic memory and optical Memory	[7]
2.7.	a) Explain different design issues for interrupt driven I/O	[8]
	b) Compare SISD Vs MISD.	[7]
2.8. G	ive short notes on (Each Carries 5 Marks)	[15]
	a) CISC	100000
	b) WORM	
	c) System Bus	
	avatachina and	
		945000470
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Page 1 of 1

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\$5003.2018	(Repeater)	Introduction to Microprocessor	To	3101	70
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- Inestion No. 1 is eximpulsed
- A compt any discreptions from Quastion No.2 to Quartical No.8 is used to upon inclusive stall marks.

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Day & Date	Somester	Subject Name	Time	Code	Marks
Saturday 30/03/2019	III Repeater	Introduction To Microprocessor	11.00 AM to 01.30 PM	3101	75

Q. No. 1 is compulsory.

Q.1 a) What are different functions and operations performed by a microprocessor? b) What are vectored interrupts? Explain 8085 vectored interrupts.	(8) (7)	
Q.2 a) Explain flag registers with diagram. b) Explain the types of RAM and ROM	(8) (7)	
<ul><li>Q.3 a) Draw and explain the pin diagram of 8085 microprocessor in detail.</li><li>b) Draw and explain system bus structure.</li></ul>	(10) (5)	
<ul> <li>Q.4 a) Explain 8237 DMA controller with internal register.</li> <li>b) Explain Arithmetic instruction set of 8085 microprocessor.</li> </ul>	(8) (7)	
Q.5 a) What are addressing modes? Explain different types of addressing modes in 8085 microprocessor.  b) What are the different types of serial communication? Explain with	(3) (7)	
Q.6 a) Explain the function of following pins of 8085 microprocessor.  i) IO/M ii) RESETIN iii) ALE iv) HOLD	(8) (7)	
b) Explain logical instruction set of 8085 microprocessor.	(15)	
Q.7 a) Write short note on: i) ECC ii) EDO iii) SIMM		
<ul> <li>Q.8 a) State the characteristics of 80286 and 80386 microprocessor.</li> <li>b) Explain the internal block diagram of 8259 interrupt controller.</li> </ul>	(8) (7)	

Day & Date	Semester	Subject Name	Time	Code	Marks
Friday 08/11/2019	III	Introduction to Microprocessor	11.00 AM to 01.30 PM	3101	75

- Q. No. 1 is compulsory.
- II) Attempt any 4 questions from Q.2 to Q.8.

Q1)		Draw and Explain architecture of 8085	15
Q2)	a)	Explain call and jump on condition instruction	10
	b)	Write short note on 4004	5
Q3)	a)	Write short note on Integrate Circuit	. 8
	b)	What are different functions and operations performed by a microprocessor?	7 .
Q4)	a)	Explain Flag register	7
	b)	Write a short note on different types of addressing mode	8
Q5)	a)	Write is interrupt? How microprocessor response on interruption	10
	b)	Explain ISR, PR and IRR of 8259	5
Q6)	a)	Write short note on DMA	5
0 (4	b)	Draw and explain pin diagram of 8251 USART	10 -
Q7)	a)	Write short note on SIMM and DIMM	8
3.6	b)	Write short note on RAM	7
Q8)	a)	Write a program to transfer n values from one memory location to other	8
3.5	b)	Write a program to perform multiplication of 2 values	7

Day & Date	Semester	Subject Name	Time	Code	Marks
Thursday 14/11/2019	III (Fresh)	Computer Organization and Architecture	11.00 AM To 01.30 PM	3103 -	75

Instruction: 1) Q 1 Is Compulsory.	
2) Attempt any 4 from Q. 2 To Q. 4.	
3) Each Question Carry 15 Marks.	
1) Write short notes on the following.	
a) Bus interconnection structure	7
b) Cache memory	8
2) a) Explain interrupt driven I/o with flow chart.	8
b) Discuss characteristics of magnetic disk?	7
3) a) Draw and explain basic instruction cycle?	7
b) Explain functions of I/O module?	8
4) a) Describe the memory hierarchy with diagram?	. 8
b) Differentiate between magnetic tape and magnetic disk?	7
5) a) What is RISC? Describe RISC pipelining with suitable diagram?	8
b) Discuss DMA function?	7
6) a) Differentiate between RAM and ROM.	8
b) Explain basic components of computer with diagram?	7
a) Describe processor parallel processing (SISD and SIMD)	8
b) Explain optical memory in detail?	7
8) Write short note on.	15
a) CISC	
b) WORM	
c) EPROM and EEPROM	

Day & Date	Semester	Subject Name	Time	Code	Marks
Saturday 16/11/2019	III (Fresh)	File Structure and Database Management	11.00 AM To 01.30 PM	3104	75

•		
	Q.1) A) Define the following terms	10
	1) Timestamp 2) Hashing 3) Transaction 4)Deadlock	
	5) Dense index	
	B) Write a short note on- Query Tree	5
	Q.2) A) Explain ACID Properties of Transaction	8
	B) Explain steps of Query processing	7
	Q.3) A) Explain Transaction state with diagram	8
	B) What is indexing? Explain Primary index with suitable example.	7
	Q.4) A)Explain last update problem and temporary update proble with example.	m 8
	B) Explain Thomas Write Rule.	7
	Q.5) A) What is lock? Explain types of locks.	8
	B) Explain Materialization and Pipelining.	7
	Q.6) A) What is hashing? Explain static & Dynamic hashing.	10
	B) Write a short note on System log.	5

Q.7) A) Simplify the query & translate it into a query tree and optimize it.	10
Relation: student(studid,name,age,deptno)	
Dept(deptno,deptname)	
Query: Display list of those students whose age is < working in the Student Council Department.	25 and
B) Explain Starvation of locks.	5
Q.8) A) Explain the deadlock detection and recovery technique.	10
B) Write a short note on-Compatibility Matrix.	5

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Day & D	ate Somester	Subject Name	Times	U DOOR W	las artes
Thursday 0.0 trigo	17 H (Fresh / Repeater)	Introduction to Microprocessor	11.00 AN To 01.30 PM	3101	75
Q.1 (1)	What are different Which are the type	tunctions and operations per s of senal communication?	formed by a micro	2	06 07
	What are two differ Explain ing registe	ent methods of interfacing M rs with diagram	Oute the microproc	essor?	08
Ĩ	Explain following 1) 197 B 2) STA 2500 3) LDA 2050 4) OUT 01H //hat are vectored	instructions:	eptored interrupt	s 1	08
D1 \$	itale the characte	the pin diagram of 8035 mi	essor.	3	10 05
	xplain De interna xplain Tiellypes c	Ittock biagram (fle259 in R/14 (rid (fl. 1)	errupt controller	3	08
t) W	2) PE WTH 3) ALE 4) FOLD	ssing modes? Explain diffe		ar Businci	07
		controller with internating		5	08
(d.E a) W	rite short note or ECC SILMA Address decodin	3		A CONTRACTOR OF THE PARTY OF TH	15
	•	\$			
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Day & Date	Semester	Subject Name	Time	Code	Marks
Thursday 14/11/2019	III (Fresh)	Computer Organization and Architecture	11.00 AM To 01.30 PM	3103	75

Instruction:	1) Q 1 Is Compulsory.			
	2) Attempt any 4 from Q. 2 To Q. 4.			
	3) Each Question Carry 15 Marks.	27		
1) Write short n	otes on the following.			
a) Bus interco	onnection structure	7		
b) Cache mer	mory	8		
2) a) Explain interrupt driven I/o with flow chart.				
b) Discuss characteristics of magnetic disk?				
3) a) Draw and	explain basic instruction cycle?	7		
b) Explain fur	nctions of I/O module?	8		
4) a) Describe th	ne memory hierarchy with diagram?	8		
b) Differentia	te between magnetic tape and magnetic disk?	7		
5) a) What is RI	SC? Describe RISC pipelining with suitable diagram?	8		
b) Discuss Di	MA function?	7		
6) a) Differentia	te between RAM and ROM.	8		
b) Explain ba	sic components of computer with diagram?	7		
7) a) Describe p	rocessor parallel processing (SISD and SIMD)	8		
b) Explain op	tical memory in detail?	7		

- a) CISC
- b) WORM

8) Write short note on.

c) EPROM and EEPROM

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15

Day & Date	Semester	Subject Name	Time	Code	Marks
Saturday 16/11/2019	III (Fresh)	File Structure and Database Management	11.00 AM To 01.30 PM	3104	75

	Q.1) A) Define the following terms	10
	1) Timestamp 2) Hashing 3) Transaction 4)Deadlock	
	5) Dense index	
	B) Write a short note on- Query Tree	5
	Q.2) A) Explain ACID Properties of Transaction	8
	B) Explain steps of Query processing	7
	Q.3) A) Explain Transaction state with diagram	8
	B) What is indexing? Explain Primary index with suitable example.	7
-	Q.4) A)Explain last update problem and temporary update problem with example.	500
	B) Explain Thomas Write Rule.	7
	Q.5) A) What is lock? Explain types of locks.	8
	B) Explain Materialization and Pipelining.	7
	Q.6) A) What is hashing? Explain static & Dynamic hashing.	10
	Q.6) A) What is hashing: Explain state of B) Write a short note on System log.	5

Day & Date	Semester	Subject Name	Time	Code	Marks
Friday 08/11/2019	III (Fresh)	Introduction to Microprocessor	11.00 AM to 01.30 PM	3101	75

- Q. No. 1 is compulsory.
- II) Attempt any 4 questions from Q.2 to Q.8.

	Draw and Explain architecture of 8085	15
	The same Jump on condition manachon	10
ь)	Write short note on 4004	5
a)	Write short note on Integrate Circuit	8
b)		7
a)	Explain Flag register	7
b)	Write a short note on different types of addressing mode	8
a)	Write is interrupt? How microprocessor response on interruption	10
b)	Explain ISR, PR and IRR of 8259	5
a)	Write short note on DMA	5
b)	Draw and explain pin diagram of 8251 USART	10
a)	Write short note on SIMM and DIMM	
6)	Write short note on RAM	8 7
a)	Write a program to transfer n values from one memory location to other	
	Write a program to perform multiplication of 2 values	- 8
	b) a) b) a) b) a) b)	a) Explain call and jump on condition instruction b) Write short note on 4004  a) Write short note on Integrate Circuit b) What are different functions and operations performed by a microprocessor?  a) Explain Flag register b) Write a short note on different types of addressing mode  a) Write is interrupt? How microprocessor response on interruption b) Explain ISR, PR and IRR of 8259  a) Write short note on DMA b) Draw and explain pin diagram of 8251 USART  a) Write short note on SIMM and DIMM b) Write short note on RAM  Write a program to transfer n values from one memory location to other

Page 1 of 1

Day & Date	Semester	Subject Name	Time	Code	Marks
Saturday 16/11/2019	III (Fresh)	File Structure and Database Management	11.00 AM To 01.30 PM	3104	75

	**
Q.1) A) Define the following terms	10
1) Timestamp 2) Hashing 3) Transaction 4)Deadlock	
5) Dense index	
B) Write a short note on- Query Tree	5
Q.2) A) Explain ACID Properties of Transaction	8
B) Explain steps of Query processing	7
Q.3) A) Explain Transaction state with diagram	/- 8
<ul> <li>B) What is indexing? Explain Primary index with suitable example.</li> </ul>	7
Q.4) A)Explain last update problem and temporary update problem	lem 8
with example.	200
B) Explain Thomas Write Rule.	7
Q.5) A) What is lock? Explain types of locks.	8
B) Explain Materialization and Pipelining.	7
Q.6) A) What is hashing? Explain static & Dynamic hashing.	10
B) Write a short note on System log.	5

Day & Date	Semester	Subject Name	Time	Code	Marks
Saturday 16/11/2019	III (Fresh)	File Structure and Database Management	11.00 AM To 01.30 PM	3104	75

2) Attempt any four questions from Q.2 to Q.8

Q.1) A) Define the following terms	10
1) Timestamp 2) Hashing 3) Transaction 4)Deadlock	
5) Dense index	1.0
B) Write a short note on- Query Tree	5
Q.2) A) Explain ACID Properties of Transaction	8
B) Explain steps of Query processing	7
Q.3) A) Explain Transaction state with diagram	8
B) What is indexing? Explain Primary index with suitable example.	7
Q.4) A)Explain last update problem and temporary update proble with example.	em 8
B) Explain Thomas Write Rule.	7
Q.5) A) What is lock? Explain types of locks.	8
B) Explain Materialization and Pipelining.	7
Q.6) A) What is hashing? Explain static & Dynamic hashing.	10
B) Write a short note on System log.	5

Page 1 of 2

Day & Date	Semester	Subject Name	Time	Code	Marks
Thursday 14/11/2019	III (Fresh)	Computer Organization and Architecture	11.00 AM To 01.30 PM	3103	75

#### Instruction: 1) Q 1 Is Compulsory. 2) Attempt any 4 from Q. 2 To Q. 4. Each Question Carry 15 Marks. 1) Write short notes on the following. a) Bus interconnection structure b) Cache memory a) Explain interrupt driven I/o with flow chart. b) Discuss characteristics of magnetic disk? a) Draw and explain basic instruction cycle? b) Explain functions of I/O module? 4) a) Describe the memory hierarchy with diagram? b) Differentiate between magnetic tape and magnetic disk? 5) a) What is RISC? Describe RISC pipelining with suitable diagram? 7 b) Discuss DMA function? a) Differentiate between RAM and ROM. b) Explain basic components of computer with diagram? 7) a) Describe processor parallel processing (SISD and SIMD) 7 b) Explain optical memory in detail? 15 8) Write short note on. a) CISC b) WORM

c) EPROM and EEPROM

Day & Date	Semester	Subject Name	Time	Code	Marks
Thursday 14/11/2019	III (Fresh)	Computer Organization and Architecture	11.00 AM To 01.30 PM	3103	75

1	nstruction:	1) Q 1 ls Compulsory.		
		2) Attempt any 4 from Q. 2 To Q. 4.	Ī	
	12	3) Each Question Carry 15 Marks.		
1	) Write short n	otes on the following.		
	a) Bus interco	onnection structure	7	
	b) Cache mer	mory	8	
2	) a) Explain int	errupt driven I/o with flow chart.	- 8	
	b) Discuss cha	aracteristics of magnetic disk?	7	
3	a) Draw and e	explain basic instruction cycle?	7	
	b) Explain fun	actions of I/O module?	8	
4	a) Describe th	ne memory hierarchy with diagram?	8	
	b) Differentiat	te between magnetic tape and magnetic disk?	7	
5)	a) What is RIS	C? Describe RISC pipelining with suitable diagram?	8	
	b) Discuss DM	MA function?	7	
6)	a) Differentiat	te between RAM and ROM.	8	
		ic components of computer with diagram?	7	
		ocessor parallel processing (SISD and SIMD)	8	
1350		ical memory in detail?	7	
	Write short no		15	
100				

- a) CISC
- b) WORM
- c) EPROM and EEPROM

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Day 9 D-4-	Competer	Subject Name	Time	Code	Marks
Day & Date Friday	III	Introduction to	11.00 AM to	3101	75
08/11/2019	(Fresh)	Microprocessor	01.30 PM		

	** **	uon.	
		I) Q. No. 1 is compulsory.	
	/	II) Attempt any 4 questions from Q.2 to Q.8.	
Q1)		Draw and Explain architecture of 8085	15
025	1	Explain call and jump on condition instruction	10
Q2)	a) b)	Write short note on 4004	5
	,,	Wille Short hote on 4001	
Q35	a)	Write short note on Integrate Circuit	8
7	b)	What are different functions and operations performed by a microprocessor?	7
~		Devilete Flore annietos	. 7
Q4)	a)	Explain Flag register	8
	b)	Write a short note on different types of addressing mode	1000
Q5)	a)	Write is interrupt? How microprocessor response on interruption	, 10
~/	b)	Explain ISR, PR and IRR of 8259	5
~~		W. th. about note on DMA	5
000	a)	Write short note on DMA	10
1	b)	Draw and explain pin diagram of 8251 USART	
Q7)	a)	Write short note on SIMM and DIMM	8
(v)		Write short note on RAM	7
,	ь)		
or	a)	Write a program to transfer n values from one memory location to other	8
Xo)	200	Write a program to perform multiplication of 2 values	7
0.60	<b>b</b> )	Wille a brokeni to berroum manifestation	

			Time	Code	Marks
Day & Date	Semester	Subject Name	11.00 AM		
Saturday 16/11/2019	III (Fresh)	File Structure and Database Management	To	3104	75

2) Attempt any four questions are	
Q.1) A) Define the following terms	10
1) Timestamp 2) Hashing 3) Transaction 4)Deadlock	
5) Dense index	5
B) Write a short note on- Query Tree Q.2) A) Explain ACID Properties of Transaction	8
Q.2) A) Explain ACID Properties of Propertie	7
O 2) A) Explain Transaction state with diagram	8
B) What is indexing? Explain Primary index with suitable	
Q.4) A)Explain last update problem and temporary update prowith example.	7.7
B) Explain Thomas Write Rule.	7 8
Q.5) A) What is lock? Explain types of locks.	7
B) Explain Materialization and Pipelining. Q.6) A) What is hashing? Explain static & Dynamic hashing	7.E.U
Q.6) A) What is hashing. Expression 19.  B) Write a short note on System log.	5
50	

Q.7) A) Simplify the query & translate it into a query tree and optimize it.	10
Relation: student(studid,name,age,deptno)	1400
Dept(deptno,deptname)	
Query: Display list of those students whose age is <25 working in the Student Council Department.	5 and
B) Explain Starvation of locks.	5
Q.8) A) Explain the deadlock detection and recovery technique.	10
B) Write a short note on-Compatibility Matrix.	5

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h		Cubbet None	Til Til	Res -	after the second to the second	Max. Marks
Day & Date	Semester	Subject Name	11.00	ANF	1101	
Thursday 19.11/2017	III (Eresh /	Introduction to Microprocessor	. 7	OPM	3101	75
	Repeater)					
lote: 11Q N	o 1 - compuiso	y sale wo No	Я			
2) Afte	not any 4 questi	13115 CITE COLD TO THE PARTY OF				-
11 1) Wh 2) Wh 12 a) Wh 5) Ext	nat are different for nich are the types nat are two differe clain flag register:	incliens and operations per of serial communication? and methods of interfacing to serial diagram.	rformed by a Explain.	micropr	ssor?	07
(,3 a) Exp	plain following 1) INX B 2) STA 2500 3) LDA 2050	Instructions:	vectored in	terrupts	4 3	100 M
b) Wh	et are vectored	interrupts r capielles	microproces	socin de		16
10 1 E 10	or or the character	the pin diagram of 8085 r			4,	05
1 P. 1	and became their the Printer Carlot I	al block diagram of £259 of RAM and RCM.			0	07 08
b) Wi	plain the function (17) RESWITH  3) ALE  4) HOLD hat are the addresses in 8085 miles	essing modes? Explain d	inferent type	<b>V</b> .5		07
	0007 DWA	controller with internal re instruction set of 8085 m	egister. nicmbreces	ov.		07
	rite short note	on:	S. S	· ·		The State of
	h	1.50		1		1
	1.	2	7 L		Î	
			24		40	

Day & Date	Semester	Subject Name	Time	Code	Marks
Saturday 16/11/2019	III (Fresh)	File Structure and Database Management	11.00 AM To 01.30 PM	3104	75

Q.1) A) Define the following terms	10
1) Timestamp 2) Hashing 3) Transaction 4)Deadlock	
5) Dense index	
B) Write a short note on- Query Tree	5
Q.2) A) Explain ACID Properties of Transaction	8
B) Explain steps of Query processing	7
Q.3) A) Explain Transaction state with diagram	8
<ul> <li>B) What is indexing? Explain Primary index with suitable example.</li> </ul>	7
Q.4) A)Explain last update problem and temporary update proble with example.	em 8
B) Explain Thomas Write Rule.	7
Q.5) A) What is lock? Explain types of locks.	8
B) Explain Materialization and Pipelining.	7
Q.6) A) What is hashing? Explain static & Dynamic hashing.	10
B) Write a short note on System log.	5

Q.7) A) Simplify the query & translate it into a query tree and optimize it.	10
Relation: student(studid,name,age,deptno)	
Dept(deptno,deptname)	
Query: Display list of those students whose age is <2: working in the Student Council Department.	5 and
B) Explain Starvation of locks.	5
Q.8) A) Explain the deadlock detection and recovery technique.	10
B) Write a short note on-Compatibility Matrix.	5

Day & Date	Semester	Subject Name	Time	Code	Marks
Thursday 14/11/2019	III (Fresh)	Computer Organization and Architecture	11.00 AM To 01.30 PM	3103	75

Instruction:	

- 1) Q 1 Is Compulsory.
- 2) Attempt any 4 from Q. 2 To Q. 4.
- 3) Each Question Carry 15 Marks.
- 1) Write short notes on the following.

a) Bus interconnection structure		7
b) Cache memory		8
2) a) Explain interrupt driven I/o with flow chart.		8
b) Discuss characteristics of magnetic disk?		7
3) a) Draw and explain basic instruction cycle?		7
b) Explain functions of I/O module?	(5)	8
4) a) Describe the memory hierarchy with diagram?		8
b) Differentiate between magnetic tape and magnetic disk?		7
5) a) What is RISC? Describe RISC pipelining with suitable diagram?		8
b) Discuss DMA function?		7
6) a) Differentiate between RAM and ROM.		8
b) Explain basic components of computer with diagram?		7
7) a) Describe processor parallel processing (SISD and SIMD)		8
b) Explain optical memory in detail?		7
8) Write short note on.		15
a) CISC		
b) WORM		

c) EPROM and EEPROM

Saturday 16/11/2019	Samastar				
	Cemester	Subject Name	Time	Code	Marks
	III (Fresh)	File Structure and Database Management	11.00 AM To 01.30 PM	3104	75

Q.1) A) Define the following terms	10
1) Timestamp 2) Hashing 3) Transaction 4)Deadlock	
5) Dense index	17
B) Write a short note on- Query Tree	5
Q.2) A) Explain ACID Properties of Transaction	8
B) Explain steps of Query processing	7
Q.3) A) Explain Transaction state with diagram	8
<ul> <li>B) What is indexing? Explain Primary index with suitable example.</li> </ul>	7
Q.4) A)Explain last update problem and temporary update proble with example.	em 8
B) Explain Thomas Write Rule.	7
Q.5) A) What is lock? Explain types of locks.	8
B) Explain Materialization and Pipelining.	7
Q.6) A) What is hashing? Explain static & Dynamic hashing.	10
B) Write a short note on System log.	5
- the second sec	

Q.7) A) Simplify the query & translate it into a query tree and optimize it.	10
Relation: student(studid,name,age,deptno)	
Dept(deptno,deptname)	
Query: Display list of those students whose age is <25 working in the Student Council Department.	and
B) Explain Starvation of locks.	5
Q.8) A) Explain the deadlock detection and recovery technique.	10
B) Write a short note on-Compatibility Matrix.	5

Day & Date	Semester	Subject Name	Time	Code	Marks
Saturday 16/11/2019	III (Fresh)	File Structure and Database Management	11.00 AM . To 01.30 PM	3104	75

	Q.1) A) Define the following terms	10
	1) Timestamp 2) Hashing 3) Transaction 4)Deadlock	
	5) Dense index	
	B) Write a short note on- Query Tree	5
	Q.2) A) Explain ACID Properties of Transaction	8
	B) Explain steps of Query processing	7
	Q.3) A) Explain Transaction state with diagram	8
	<ul> <li>B) What is indexing? Explain Primary index with suitable example.</li> </ul>	7
	Q.4) A)Explain last update problem and temporary update proble with example.	em 8
	B) Explain Thomas Write Rule.	7
	Q.5) A) What is lock? Explain types of locks.	8
	B) Explain Materialization and Pipelining.	7
	Q.6) A) What is hashing? Explain static & Dynamic hashing.	10
* 1	B) Write a short note on System log.	5

Q.7) A) Simplify the query & translate it into a query tree and optimize it.	10
Relation: student(studid,name,age,deptno)	
Dept(deptno,deptname)	
Query: Display list of those students whose age is <2: working in the Student Council Department.	5 and
B) Explain Starvation of locks.	5
Q.8) A) Explain the deadlock detection and recovery technique.	10
B) Write a short note on-Compatibility Matrix.	5

Day & Date	Semester	Subject Name	Time	Code	Marks
Saturday 16/11/2019	III (Fresh)	File Structure and Database Management	11.00 AM To 01.30 PM	3104	75

Q.1) A) Define the following terms	10
1) Timestamp 2) Hashing 3) Transaction 4)Deadlock	
5) Dense index	
B) Write a short note on- Query Tree	5
Q.2) A) Explain ACID Properties of Transaction	8
B) Explain steps of Query processing	7
Q.3) A) Explain Transaction state with diagram	8
B) What is indexing? Explain Primary index with suitable example.	7
Q.4) A)Explain last update problem and temporary update problem with example.	lem 8
B) Explain Thomas Write Rule.	7
Q.5) A) What is lock? Explain types of locks.	8
B) Explain Materialization and Pipelining.	7
(2.6) A) What is hashing? Explain static & Dynamic hashing.	10
B) Write a short note on System log.	5

Q.7) A) Simplify the q optimize it.	uery & translate it into a query tree and	10
Relation: s	tudent(studid,name,age,deptno)	10
	Dept(deptno,deptname)	
Query: Dis	play list of those students whose age is < the Student Council Department.	25 and
B) Explain Starva	tion of locks.	5
	adlock detection and recovery technique.	10
B) Write a short ne	ote on-Compatibility Matrix	5

Day & Date	Semester	Subject Name	Time	Code	Marks
Saturday 16/11/2019	III (Fresh)	File Structure and Database Management	11.00 AM	3104	75

Q.1) A) Define the following terms	- 10
1) Timestamp 2) Hashing 3) Transaction 4)Deadlock	
5) Dense index	
B) Write a short note on- Query Tree	5
Q.2) A) Explain ACID Properties of Transaction	8
B) Explain steps of Query processing	7
Q.3) A) Explain Transaction state with diagram	8
B) What is indexing? Explain Primary index with suitable example.	7
Q.4) A)Explain last update problem and temporary update proble with example.	em 8
B) Explain Thomas Write Rule.	7
Q.5) A) What is lock? Explain types of locks.	8
B) Explain Materialization and Pipelining.	7
Q.6) A) What is hashing? Explain static & Dynamic hashing.	10
B) Write a short note on System log.	5

Q.	<ol> <li>A) Simplify the query &amp; translate it into a query tree and optimize it.</li> </ol>	10
	Relation: student(studid,name,age,deptno)	
	Dept(deptno,deptname)	Ť
	Query: Display list of those students whose age is <25 working in the Student Council Department.	and
	B) Explain Starvation of locks.	5
Q.	8) A) Explain the deadlock detection and recovery technique.	10
	B) Write a short note on-Compatibility Matrix	5

Day & Date	Semester	Subject Name	Time	Code	Marks
Thursday 14/11/2019	III (Fresh)	Computer Organization and Architecture	11.00 AM To 01.30 PM	3103	75

Instruction:	1) Q 1 ls Compulsory.			
	2) Attempt any 4 from Q. 2 To Q. 4.	(3		
	3) Each Question Carry 15 Marks.			
1) Write short no	otes on the following.			
a) Bus interco	nnection structure		7	
b) Cache mem	nory		8	
2) a) Explain inte	errupt driven I/o with flow chart.		8	
b) Discuss cha	racteristics of magnetic disk?		7	
3) a) Draw and ex	xplain basic instruction cycle?		7	
b) Explain fund	ctions of I/O module?	14 14	8	
4) a) Describe the	e memory hierarchy with diagram?		8	
b) Differentiate	between magnetic tape and magnetic disk?		7	
5) a) What is RISC	? Describe RISC pipelining with suitable diagram?		8	
b) Discuss DM	A function?		7	
6) a) Differentiate	between RAM and ROM.		8	
	c components of computer with diagram?		7	
	cessor parallel processing (SISD and SIMD)		8	
	al memory in detail?	8	7	
8) Write short not			15	
a) CISC				
b) WORM				
c) EPROM and	EEPROM		9	

Day & Date	Semester	Subject Name	Time	Code	Marks
Thursday 14/11/2019	III (Fresh)	Computer Organization	11.00 AM To 01.30 PM	3103 .	75

Instruction:	1) Q 1 Is Compulsory.		
	2) Attempt any 4 from Q. 2 To Q. 4.	*	
10	3) Each Question Carry 15 Marks.		
1) Write short ne	otes on the following.		
a) Bus interco	nnection structure		7
b) Cache men	nory		- 8
2) a) Explain inte	errupt driven I/o with flow chart.		8
b) Discuss cha	racteristics of magnetic disk?		7
3) a) Draw and e	xplain basic instruction cycle?		7
b) Explain fund	ctions of I/O module?	7/	8
4) a) Describe the	e memory hierarchy with diagram?	9	8
.b) Differentiate	e between magnetic tape and magnetic disk?		7
5) a) What is RISO	C? Describe RISC pipelining with suitable diagram?		8
b) Discuss DM	A function?		7
6) a) Differentiate	between RAM and ROM.		8
b) Explain basi	c components of computer with diagram?		7
7) a) Describe pro	ocessor parallel processing (SISD and SIMD)		8
b) Explain optio	cal memory in detail?	100	7
8) Write short not	te on.		15
a) CISC b) WORM c) EPROM and	d EEPROM	6	

Day & Date	Semester	Subject Name	Time	Code	Marks
Thursday 14/11/2019	III (Fresh)	Computer Organization and Architecture	11.00 AM To 01.30 PM	3103	75

Instruction:	1) Q 1 Is Compulsory.		
	2) Attempt any 4 from Q. 2 To Q. 4.		
	3) Each Question Carry 15 Marks.		
1) Write short n	otes on the following.		
a) Bus interco	nnection structure	7.5	7
b) Cache men	nory		8
2) a) Explain int	errupt driven I/o with flow chart.		8
b) Discuss cha	racteristics of magnetic disk?		7
3) a) Draw and e	explain basic instruction cycle?		7
b) Explain fun	ctions of I/O module?	2.5	8
4) a) Describe th	e memory hierarchy with diagram?		8
b) Differentia	te between magnetic tape and magnetic disk?		7
5) a) What is RIS	C? Describe RISC pipelining with suitable diagram?		8
b) Discuss DN	1A function?		7
6) a) Differential	te between RAM and ROM.		8
b) Explain bas	ic components of computer with diagram?		7
7) a) Describe po	ocessor parallel processing (SISD and SIMD)		. 8
b) Explain opt	ical memory in detail?		7
8) Write short n			15
a) CISC			
b) WORM			
c) EDBOM a	nd FEPROM		

Day 9 Data	Competer	Subject Name	Time	Code	Marks
Day & Date	Semester	Subject Hame	11.00 AM		
Thursday 14/11/2019	III (Fresh)	Computer Organization and Architecture	To 01.30 PM	3103	75

Instruction:	1) Q 1 Is Compulsory.		
	2) Attempt any 4 from Q. 2 To Q. 4.	60	
	3) Each Question Carry 15 Marks.		
1) Write short n	otes on the following.		
a) Bus interco	onnection structure		7
b) Cache mer	mory .		8
2) a) Explain int	errupt driven I/o with flow chart.		8
b) Discuss cha	racteristics of magnetic disk?		7
0.00	explain basic instruction cycle?		7
	ctions of I/O module?		8
4.540	e memory hierarchy with diagram?		8
	e between magnetic tape and magnetic disk?		7
	C? Describe RISC pipelining with suitable diagram?		8
b) Discuss DM			7
Contract Contract Contract Contract	e between RAM and ROM.		8
	ic components of computer with diagram?		7
	ocessor parallel processing (SISD and SIMD)		8
	ical memory in detail?		7
8) Write short no			15
a) CISC			
b) WORM			
-,			

c) EPROM and EEPROM

Day & Date	Samostan				
	Semester	Subject Name Time		Code	Marks
Thursday 14/11/2019	III (Fresh)	Computer Organization and Architecture	11.00 AM To 01.30 PM	3103	75

Instruction:	1) Q 1 Is Compulsory.			
9	2) Attempt any 4 from Q. 2 To Q	), 4.	91.00	
95	3) Each Question Carry 15 Marl	ks.		
1) Write short n	otes on the following.			
a) Bus interco	nnection structure			7
b) Cache men	nory			8
2) a) Explain inte	errupt driven I/o with flow chart.			8
b) Discuss cha	racteristics of magnetic disk?			7
3) a) Draw and e	xplain basic instruction cycle?			7
b) Explain fund	ctions of I/O module?		110	8
4) a) Describe the	memory hierarchy with diagram?	40.00	21 - E	8
b) Differentiate	between magnetic tape and magn	etic dis <mark>k?</mark>		7
5) a) What is RISC	? Describe RISC pipelining with su	itable diagram?		8
b) Discuss DM/	A function?			7
6) a) Differentiate	between RAM and ROM.			8
	components of computer with dia	gram?		7
	cessor parallel processing (SISD ar			8
	al memory in detail?		2	7
8) Write short note				15
a) CISC	32	130		
b) WORM				

c) EPROM and EEPROM

Day & Date	Semester	Subject Name	Time	Code	Marks
Thursday 14/11/2019	III (Fresh)	Computer Organization and Architecture	11.00 AM To 01.30 PM	3103	75

	0.			
Instruction:	1) Q 1 Is Compulsory.			
	2) Attempt any 4 from Q. 2 To Q. 4.	14.5		
	3) Each Question Carry 15 Marks.			
1) Write short n	otes on the following.			
a) Bus interco	onnection structure		7	
b) Cache mer	nory		8	
2) a) Explain int	errupt driven I/o with flow chart.		8	
b) Discuss cha	aracteristics of magnetic disk?		7	
3) a) Draw and o	explain basic instruction cycle?		7	
b) Explain fur	ections of I/O module?		8	
4) a) Describe th	e memory hierarchy with diagram?		8	
b) Differentia	te between magnetic tape and magnetic disk?		7	
5) a) What is RIS	C? Describe RISC pipelining with suitable diagram?		8	
b) Discuss DN	AA function?		7	
6) a) Differentia	te between RAM and ROM.		8	
b) Explain bas	sic components of computer with diagram?		7	
7) a) Describe pr	ocessor parallel processing (SISD and SIMD)		8	
	ical memory in detail?	- 1	7	
8) Write short no			15	5
e) CICC				

- a) CISC
- b) WORM
- c) EPROM and EEPROM

Day & Date	Semester	Subject Name	Time	Code	Marks
Thursday 14/11/2019	III (Fresh)	Computer Organization and Architecture	11.00 AM To 01.30 PM	3103	75

Instruction:	1) Q 1 Is Compulsory.			
	2) Attempt any 4 from Q. 2 To Q. 4.	100		
	3) Each Question Carry 15 Marks.			
1) Write short n	otes on the following.			
THE STOREST CONTRACTOR	nnection structure			7
b) Cache men	nory			8
2) a) Explain into	errupt driven I/o with flow chart.			8
	racteristics of magnetic disk?			7
C. 2000-000-000-000-00	xplain basic instruction cycle?			7
	ctions of I/O module?			8
	e memory hierarchy with diagram?			8
	e between magnetic tape and magnetic disk?			7
	C? Describe RISC pipelining with suitable diagram?			8
b) Discuss DM				7
	e between RAM and ROM.			8
	c components of computer with diagram?			7
	ocessor parallel processing (SISD and SIMD)			8
	cal memory in detail?			7
8) Write short no				15
55 55000			3.5	
a) CISC b) WORM				
c) EPROM an	d EEPROM			

Day & Date	Semester	Subject Name	Time	Code	Marks
Thursday 14/11/2019	III (Fresh)	Computer Organization and Architecture	11.00 AM To 01.30 PM	3103	75

# Instruction: 1) Q 1 Is Compulsory. 2) Attempt any 4 from Q. 2 To Q. 4.

3) Each Question Carry 15 Marks.

<ol> <li>Write short notes on the following.</li> </ol>		
a) Bus interconnection structure		7
b) Cache memory		8
2) a) Explain interrupt driven I/o with flow chart.		8
b) Discuss characteristics of magnetic disk?		. 7
3) a) Draw and explain basic instruction cycle?		7
b) Explain functions of I/O module?	1.7	8
4) a) Describe the memory hierarchy with diagram?		8
b) Differentiate between magnetic tape and magnetic disk?		7
5) a) What is RISC? Describe RISC pipelining with suitable diagram?		8
b) Discuss DMA function?		7
6) a) Differentiate between RAM and ROM.		8
b) Explain basic components of computer with diagram?		7
7) a) Describe processor parallel processing (SISD and SIMD)		. 8
b) Explain optical memory in detail?		7
8) Write short note on.		15
a) CISC		
b) WORM		
c) EPROM and EEPROM		